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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Application Number	10/066,456
				Filing Date	1/31/02
				First Named Inventor	Steven Teig, et al.
				Group Art Unit	2825
				Examiner Name	B. Tat
Sheet	1	of	2	Attorney Docket Number	SPLX.P0099

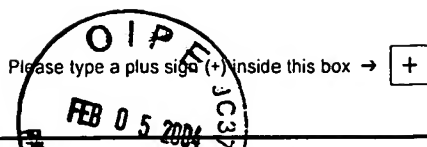
U.S. PATENT DOCUMENTS						
Examiner* Initials	Cite No. ¹	U.S. Patent Document		Date of Publication MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
	1.	2002/0157063	A1	10-24-2002	Besson	
	2.	2002/0178432	A1	11-28-2002	Kim et al.	
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	18.	6,519,754	B1	02-11-2003	McElvain et al.	
	19.	6,519,609	B1	02-11-2003	Touzet	
	20.	6,539,536	B1	03-25-2003	Singh et al.	
	21.	6,543,037	B1	04-01-2003	Limqueco et al.	

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22.	6,546,541	B1	04-08-2003	Petranovic et al.	
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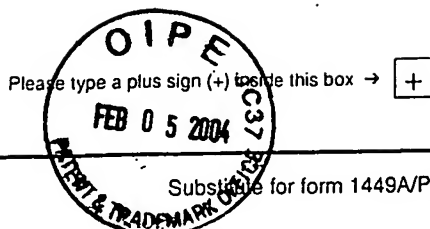
NON PATENT LITERATURE DOCUMENTS					
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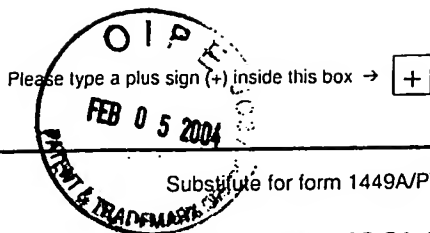
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	1.	5,311,442		05-10-1994	Fukushima	
	2.	5,519,630		05-21-1996	Nishiyama et al.	
	3.	5,521,835		05-28-1996	Trimberger	
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	15.	6,334,205	B1	12-25-2001	Iyer et al.	
	16.	6,336,208		01-01-2002	Mohan et al	
	17.	6,470,478	B1	10-22-2002	Bargh et al.	
	18.	6,473,884		10-29-2002	Ganai et al	
	19.	6,496,972	B1	12-17-2002	Segal	
	20.	6,523,156	B2	02-18-2003	Cirit	
	21.	6,574,779	B2	06-03-2003	Allen et al.	

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NON PATENT LITERATURE DOCUMENTS					
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	22.	Crastes, M. et al., A technology Mapping Method Based on Perfect and Semi-Perfect Matchings, Design Automation Conference, 1991, 28 th ACM/IEEE, June 17-21, 1991, pp. 93-98.			
	23.	Devadas, S. et al., Synthesis and Optimization for Robustly Delay-fault Testable Combinational Logic Circuits, Design Automation Conference, 1990, 27 th ACM/IEEE, June 24-28, 1990, pp.221-227.			
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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Sheet

1

of

2

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First Named Inventor

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RELATED U.S. PATENT APPLICATIONS

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	1.	10/062,017	Steven Teig & Asmus Hetzel	01-31-02	
	2.	10/061,459	Steven Teig & Asmuz Hetzel	01-31-02	
	3.	10/062,014	Steven Teig & Asmus Hetzel	01-31-02	
	4.	10/062,044	Steven Teig & Asmuz Hetzel	01-31-02	
	5.	10/066,264	Steven Teig & Asmus Hetzel	01-31-02	
	6.	10/066,188	Steven Teig & Asmus Hetzel	01-31-02	
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	8.	10/061,474	Steven Teig & Asmus Hetzel	01-31-02	
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	10.	10/062,993	Steven Teig & Asmus Hetzel	01-31-02	
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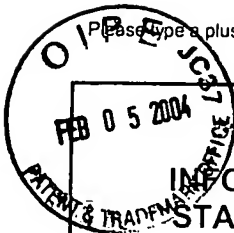
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	12.	5,649,165		Jain et al.	07-15-97	
	13.	6,301,687	B1	Jain et al.	10-09-01	

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OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS

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	14.	D. Jongeneel, R. Otten, Y. Watanabe and R. K. Brayton, Area and Search Space Control for Technology Mapping, 37 th Design Automation Conference, 86-91, 2000.	
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